

Jc892 U.S. PRO
09/654253



Form PTO-1449	LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT <small>(Use Several Sheets if Necessary)</small>	ATTY. DOCKET NO.	SERIAL NO.
		SC11244ZC	
		Cynthia L. Recker et al.	
		FILING DATE	GROUP
		September 1, 2000	

REFERENCE DESIGNATION

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	ISSUE DATE	NAME	CLASS	SUBCLASS	FILING DATE
AA						
AB						
AC						
AD						
AE						
AF						
AG						
AH						
AI						
AJ						
AK						
AL						
AM						
AN						

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	GRANT DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATION YES	NO
AO						<input type="checkbox"/>	<input type="checkbox"/>
AP						<input type="checkbox"/>	<input type="checkbox"/>
AQ						<input type="checkbox"/>	<input type="checkbox"/>

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

MRH	AR	P. Drennan, "Integrated Circuit Device Mismatch Modeling and Characterization for Analog Circuit Design" - Ph.D. dissertation, Arizona State University, May 1999, 227 pages.
MRH	AS	P. Drennan et al., "A Comprehensive MOSFET Mismatch Model", IEEE ICMS, March 1999.
	AT	

EXAMINER

DATE CONSIDERED

Feb 19, 2004

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.